SREE VIDYANIKETHAN Engineering College

SREE VIDYANIKETAN ENGINEERING COLLEGE

(AUTONOMOUS)

Sree Sainath Nagar, Tirupati- 517102

Department of Electrical and Electronics Engineering

A one day Workshop on "Protecting and Exploring IPRs"

On 09-08-2014

REPORT

A one day Workshop on "Protecting and Exploring IPRs" by Dr. V. R. Anitha, Professor, Dept. of ECE, SVEC and Mr. D. Damodaran, Assoc. Professor, Dept. of ECE, SVEC is organized for members of faculty, PG students of Electrical & Electronics Engineering and also for other departments in ED cell on 09-08-2014.

The first technical session was started with the lecture on Protecting and Exploring IPR by Dr. V.R Anitha, Professor of ECE, SVEC. She delivered the importance of Intellectual Property Rights and the problems faced by the inventor with examples. She then focused on the protection of the innovative ideas and revoke the patents of our intellectual properties with practical case studies.

Afternoon (i.e., 09.08.2014 AN), the second technical session was started with the lecture by **Mr. D. Damodaran**, Assoc Professor, Dept of ECE, SVEC, Tirupati. He explained about Trade mark, Industrial designs, Geographical Indicators, protection of plant varieties and farmers rights with case study.

Details Of Expert

1. Dr.V.R. Anitha

Professor of ECE Sree Vidyanikethan Engineering College Sree Sainath Nagar A. Rangampet Tirupathi, A.P, India

2. Mr. D. Damodaran

Assoc. Professor of ECE Sree Vidyanikethan Engineering College Sree Sainath Nagar A. Rangampet Tirupathi, A.P, India